

CURRICULUM VITAE - Alexander Hanzlik, PhD MSc MBA

Personal Information

- Name: Alexander Hanzlik
- Date of birth: 26.03.1970, Vienna, Austria
- Marital status: unmarried
- Nationality: Austria
- Military service: done (1989)

School Education

- 1976-1980: Elementary school, Vienna
- 1980-1988: Secondary school with bias on Latin and Greek, Vienna (Graduation with distinction)

Higher Education

- 2009-2011: Master of Business Administration (MBA), PEF Private University of Management, Vienna. Master Thesis: "Assessment of the Potential for Cost Savings and Quality Improvements by Integration of Simulation Environments into Automotive Development Processes". (Graduation with distinction)
- 2001-2004: PhD in Computer Science, Vienna University of Technology. PhD Thesis: "Investigation of Fault-Tolerant Multi-Cluster Clock Synchronization Strategies by Means of Simulation". (Promotion with distinction)
- 1989-1995: MSc in Computer Science, Vienna University of Technology. Master Thesis: "Entwurf und Implementierung von Patientendatenbank und Wissensbasis für das medizinische Expertensystem CADIAG-4" / "Design and Implementation of Patient DataBase and Knowledge Base for the Medical Expert System CADIAG-4".

Professional Life

Period	Organization	Role
2021-2027	Erste Digital	Technical Lead & Solution Manager
2017-2020	Erste Digital	DevOps & Operator
2015-2016	Siemens Corporate Technology (CT)	Senior Software Engineer
2009-2014	Austrian Institute of Technology (AIT)	Software Developer and Researcher
1998-2009	Siemens Program- and System Development (PSE)	Software Developer, Project Manager, Team Coordinator
1995-1998	Frequentis Group	Software Engineer

Professional & Project Experience (Details)

Erste Digital (2017-2027)

- **2021-2027: Technical Lead & Solution Manager**
 - End-to-End responsibility for the solutions C1268 AMANDA Archiv and GAI09 List-IT.
 - Professional leadership and coordination of an interdisciplinary team; steering task allocation and resource planning.
 - Acted as a subject matter expert during the cloud migration (Phase 1); supported the cloud engineering team with architectural knowledge and business requirements to ensure a smooth transition from Oracle to PostgreSQL and GCS.
 - Responsibility for incident and change management, ensuring operational stability and compliance with regulatory requirements (GDPR, audit safety).
- **2017-2020: DevOps & Operator**
 - Operational management and maintenance of critical software solutions.
 - Hands-on troubleshooting, incident management, and continuous optimization of system performance.
 - Collaborated closely with development teams to improve deployment processes and system reliability.

Siemens Corporate Technology (2015-2016)

- Project iSSN (intelligent Secondary Substation Node): Design and implementation of a remote maintenance solution for low-voltage grids (Java).
- Project WoS (Web of Systems): Architecture for IoT systems; implementation of "Device Configuration Management" (C++/Java).
- Requirements Engineering.

Austrian Institute of Technology (2009-2014)

- Research: Simulation of network architectures for the next generation of electric vehicles.
- Projects (POLLUX, MBAT, CRYSTAL, VASL): Design, validation, and analysis of complex electronic control systems for automotive and aerospace applications (Partners: PSA, Fiat, Volvo).
- Supervision of degree candidates.

Siemens Program- and System Development (1998-2009)

- **Project Management & Coordination:** Leading projects in the field of DSL network analysis (COSMO, BOAS) and coordination of sub-projects (NTS10 EWSD Internode).
- **Software Engineering:** Development of ECU control software (Magna/Audi), firmware development (Siemens Automation and Drives), and rule-based tools for protocol stacks (Java/XML).
- **Skills:** C/C++, C#, Java, Embedded Systems, Requirements Engineering, Project Management methodologies (Primas CONSULTING).

Frequentis Group (1995-1998)

- Project ARTEMIS: Software development for voice communication systems in air traffic control (Customer: STNA France).
- Activities: Software development (C), coordination of Factory Acceptance Tests, effort planning for sub-projects, and code reviews.

Research Activities

- 2003-2007: Research Assistant, FWF-funded project "Clock Synchronization Strategies".
- Publications: Focus on fault-tolerant distributed real-time systems (FlexRay, TTP/C, Time-Triggered Ethernet).
- 2002-2009: Design and implementation of SIDERA (integrated simulation environment for time-triggered real-time systems).

Other

- Languages: German (native), English (fluent)
- Driving License: Automobile